

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Mimberg, et al.

Serial No. : 10/078,292

Filed : 2/15/02 Group Art Unit: 2863

For : A METHOD AND SYSTEM FOR DYNAMIC POWER SUPPLY

VOLTAGE ADJUSTMENT FOR A SEMICONDUCTOR

Examiner: Sun, X.

INTEGRATED CIRCUIT DEVICE

RESPONSE TO NOTICE OF NON-COMPLIANT AMENDMENT

Assistant Commissioner for Patents & Trademarks Washington, D.C. 20231

Sir:

In response to the Office Action mailed 3/11/04 regarding a Notice of Non-Compliant Amendment, please consider the following resubmitted claim amendment section.

Examiner: Sun, X.

Art Unit: 2863